SK 55 TAA



SEMITOP®2

Two separated thyristors

SK 55 TAA

Target Data

Features

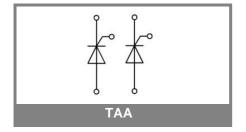
- · Compact design
- · One screw mounting
- Heat transfer and isolation through direct copper bonded aluminium oxide ceramic (DBC)
- Glass passivated thyristor chips
- Up to 1600 reverse voltage
- High surge currents

Typical Applications

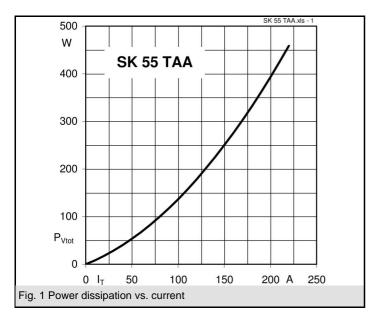
- Brake chopper
- Soft starters

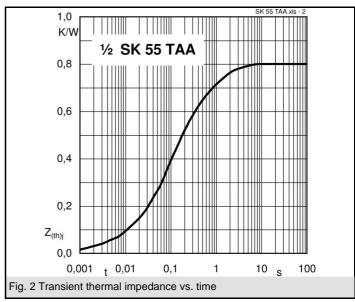
V_{RSM}	V_{RRM}, V_{DRM}	I _T = 55 A
V	V	(T _s = 80 °C)
900	800	SK55TAA08
1300	1200	SK55TAA12
1700	1600	SK55TAA16

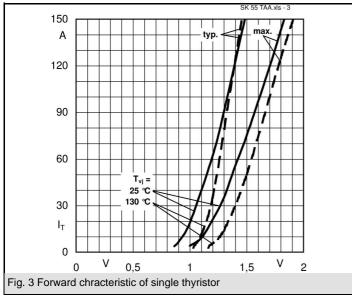
Characteristics		Ts = 25°C unless otherwise specified		
Symbol	Conditions	Values	Units	
I _T	Ts = 100°C	36	Α	
I _T	Ts = 80°C	55	Α	
			Α	
I _{TSM} /I _{FSM}	T _{vj} = 25 (125) °C; 10 ms	1000 (900)	Α	
l²t	T_{vj} = 25 (125) °C; half sine wave, 10 ms	5000 (4000)	A²s	
T _{stg}		-40 + 125	°C	
T _{solder}	terminals, 10 s	260	°C	
Thyristor	•			
(dv/dt) _{cr}	T _{vi} = 125 °C	1000	V/µs	
(di/dt) _{cr}	T _{vi} = 125 °C; f = 50 60 Hz	50	A/µs	
t_q	T _{vi} = 125 °C; typ.	80	μs	
I _H	$T_{vi} = 25 ^{\circ}\text{C}$; typ. / max.	100 / 200	mA	
I_L	$T_{vj} = 25 \text{ °C}; R_G = 33 \Omega; \text{ typ. / max.}$	200 / 500	mA	
V _T	$T_{vi} = 25 ^{\circ}\text{C}; (I_T = 80 \text{A}); \text{max}.$	1,2	V	
$V_{T(TO)}$	T _{vi} = 125 °C	max. 0,85	V	
r _T	T _{vi} = 125 °C	max. 5,7	$m\Omega$	
I _{DD} ; I _{RD}	$T_{vj}^{r,j}$ = 125 °C; $V_{DD} = V_{DRM}$; $V_{RD} = V_{RRM}$	max. 15	mA	
R _{th(j-s)}	cont. per thyristor	0,8	K/W	
T _{vi}		-40 +12 5	°C	
V _{GT}	T _{vi} = 25 °C; d.c.	2	V	
I _{GT}	T_{vi}^{yj} = 25 °C; d.c.	100	mA	
V _{GD}	T _{vi} = 125 °C; d.c.	0,25	V	
I_{GD}	T _{vj} = 125 °C; d.c.	5	mA	
Diode		•		
V_{F}	$T_{v_i} = {^{\circ}C}; (I_F = A); max.$		V	
$V_{(TO)}$	$T_{vj} = {^{\circ}C}$		V	
r _T	$T_{vj}^{s} = {}^{\circ}C$		$m\Omega$	
I_{RD}	$T_{vj} = {^{\circ}C}; V_{RD} = V_{RRM}$		mA	
R _{th(j-s)}			K/W	
T_{vj}			°C	
Mechanic	cal data			
V_{isol}	AC 50Hz, r.m.s. 1min (1sec)	2500 (3000)	V	
M_1	mounting torque	2	Nm	
w		19	g	
Case	SEMITOP®2	T 81		

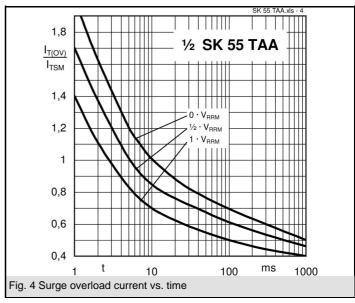


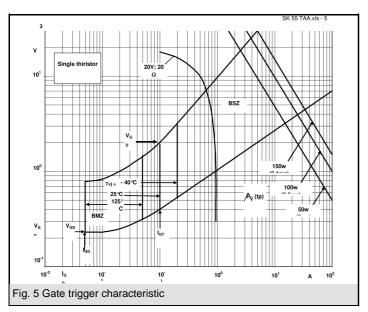
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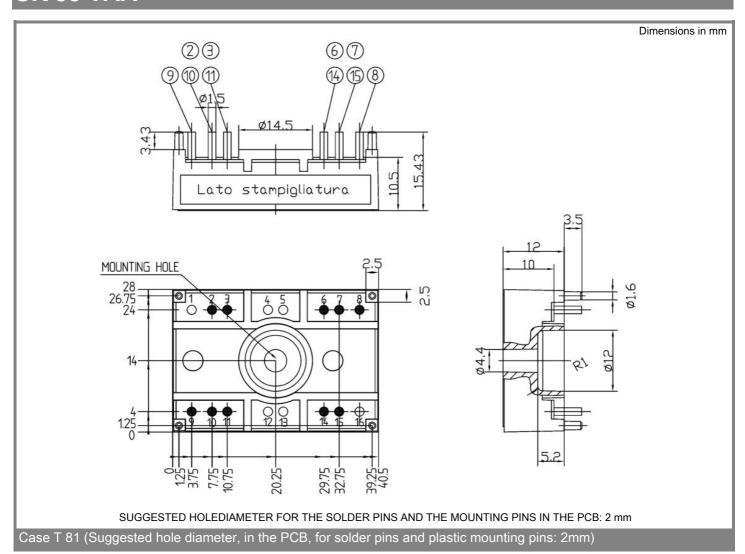


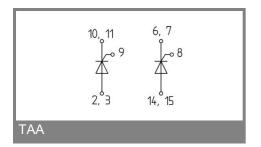












This is an electrostatic discharge sensitive device (ESDS), international standard IEC 60747-1, Chapter IX.

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